

Examiner-Initiated Interview Summary	Application No. 10/815,483	Applicant(s) TRIMBERGER ET AL.	
	Examiner Phallaka Kik	Art Unit 2825	

All Participants:

(1) Phallaka Kik.

(2) Justin Liu (Reg. No. 51,959).

Date of Interview: 2 June 2006

Type of Interview:
☒ Telephonic
☐ Video Conference
☐ Personal (Copy given to: ☐ Applicant ☐ Applicant's representative)

Exhibit Shown or Demonstrated: ☐ Yes ☒ No
 If Yes, provide a brief description:

Status of Application: pending

(3) _____

(4) _____

Time: 1:03 PM

Part I.

Rejection(s) discussed:
None

Claims discussed:
1-45

Prior art documents discussed:
None

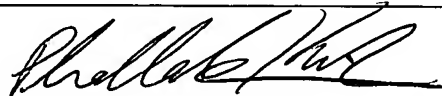
Part II.

SUBSTANCE OF INTERVIEW DESCRIBING THE GENERAL NATURE OF WHAT WAS DISCUSSED:
 See Continuation Sheet

Part III.

☐ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview directly resulted in the allowance of the application. The examiner will provide a written summary of the substance of the interview in the Notice of Allowability.

☒ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview did not result in resolution of all issues. A brief summary by the examiner appears in Part II above.



 (Examiner/SPE Signature)

 (Applicant/Applicant's Representative Signature – if appropriate)

Continuation of Substance of Interview including description of the general nature of what was discussed: May 31, 2006--Since the Examiner had not heard from Applicant's Representative regarding the restriction requirement indicated a few weeks earlier, a message is again left in Applicant Representative's voice box, indicating that the restriction requirements in which invention I, claims 1-6,20-30 drawn to a method of testing a programmable logic device involving the steps of correlating the failed signal paths with at least one alternative signal path, instantiating the alternative signal path and testing the instantiated signal path, classified in class 716, subclass 4; invention II, claims 7-13, drawn to a method of verifying the functionality of a design involving configuring at least a selected one of the plurality of programmable blocks to provide a signal-generation function, classified in class 716, subclass 5; invention III, claims 14-19, drawn to computer-readable medium that generates alternative test designs based on an express for the customer design, classified in class 716, subclass 4; invention IV, claims 31-39, drawn to a method of testing programmable logic device involving identifying a collection of signal parts that was defined in the merged expression, classified in class 714, subclass 4; invention V, claims 40-45, drawn to a method of testing programmable logic device involving testing the instantiated signal paths and the auxiliary resources specified, classified in class 714, subclass 4. These inventions are related together as subcombinations disclosed as useful together but could have separate utility, and that a separate search would be required for each invention. Applicant is further given until June 2, 2006 to respond; otherwise, a written restriction will be given, to give more time for applicant to make the election. June 2, 2006--In response to the Examiner's messages, Applicant's Representative was unable to elect the invention to prosecute and requested that a written restriction be given.